SIMPLE CHAOS GENERATOR WITH ROBUST OPERATION

Victor Cojocaru¹, Horia-Nicolai Teodorescu²

¹D. Ghitu Institute of the Electronic Engineering and Nanotechnologies of the Academy of Sciences of Moldova, Chisinau, Moldova
² Faculty of Electronics, Telecommunications and Information Technology, 'Gheorghe Asachi' Technical University of Iasi, Romania Institute of Computer Science of the Romanian Academy, Iasi, Romania <u>vcojocaru@nano.asm.md, hteodor@etti.tuiasi.ro</u>

Abstract. We describe a simple chaos generator circuit build based on the LM555 timer. This simple circuit can be assembled on a test board shortly. The circuit can be used for studying the processes related to nonlinear dynamics and to perform various measurements.

Keywords: Chaos, electronic circuit, nonlinearity, 555 timer.

I. Introduction

The chaotic dynamics is the complex non-periodic movement being generated by the nonlinear systems. This kind of movement can occur in the absence of the external noise and is completely determined by the properties of the dynamic deterministic systems [1-3]. In this paper, we study a simple chaotic dynamic generator realized using the E555 timer [4] and low tolerance components.

A good instrument used to characterize the chaotic regime is the phase portrait which consists in the graphical representation of the internal state of a system towards another. The phase diagram is easy interpretable applying the signals at X respectively Y inputs of the oscilloscope. The phase diagram method is a graphical-analytical method used to study the dynamic systems with the equations having the form:

$$\frac{dx}{dt} = P(x, y), \ \frac{dy}{dt} = Q(x, y),$$

where x and y are the system variables, P(x, y), Q(x, y) and – functions that meet the conditions of existence of the solutions, *t* – the time, as independent variable [5, 6].

The systems behavior can be represented on a rectangular plan in Cartesian coordinates. In this representation each state of a dynamic system corresponds to a point of the x and y coordinates plan and reverse, for each point of the plan corresponds to a state of the studied system. The Q(x, y) is named phase plane. The change of the studied system state is displayed on the phase plan

through displacement of a point, named phase point. The point movement trajectory is named phase trajectory, the direction and the velocity are determined by the phase vector. Is essential that each point from the phase plan is a phase trajectory only. The phase trajectories set is named portrait phase of the system and display all the combinations and the types of the possible system movements. In the phase plane are usually three types of the phase trajectories: singular points or equilibrium position determined by solving the equations system.

P(x, y) = 0 Q(x, y) = 0

Closed isolated trajectories corresponding to periodic movement in the system, separated, that divided the phase plan in zones with different types of trajectories. The phase plan method is used to build a phase portrait of the system and to analyze later this portrait. The method is proper to determine the number, the types and the nature of the singular points, the closed and isolated orbits of the points, and make possible viewing all the movements set that occur in a dynamic system for all initial possible conditions. The singular points are classified function by the nature of the phase trajectories in their vicinity. The isolated closed trajectories (limited cycles) are classified function by the nature of their stability [7-10].

II. Circuit

The diagram of the circuit is shown in Fig. 1. The circuits consist in two tightly coupled oscillators based on the venerable 555 [4]. The couplings are nonlinear and consist in the diodes D1 and D2 and one of the couplings a first inter filter. One of the couplings relates in a nonlinear manner the two periods through the diode D_1 , while the other emphasizes the higher frequencies through the circuit R_1C_1 . The value of the capacitance C_1 is important in providing the right frequency coupling of the output of IC_1 to the charging circuit of IC_2 thus favoring the chaotic behavior.

The two outputs are not alike, as illustrated in Fig. 2 and Fig. 3, left panel. A typical graphical tool for characterizing the chaotic regime is the phase plot, which consists in the graphical representation of one internal state of the system with respect to another one. In the phase plot diagram, the time is suppressed, as one state evolution is a function of the other state evolution during time.

Here, the two states are chosen to be the outputs of the two 555 timers, denoted by O_1 ands O_2 in Fig. 1. Subsequently, the phase plot corresponds to the output O_2 as a function of the output

 O_1 . The phase plot is easily displayed as the *XY* view on an oscilloscope, assigning to the oscilloscope inputs $X = O_2, Y = O_1$.



Examples of phase plots are shown in Fig. 3 (right panel), for $V_{cc} = 15V$, in Fig. 4 (right), for $V_{cc} = 16V$, and in Fig. 5 (right), for $V_{cc} = 18,5V$, for the circuit in Fig. 1. These plots demonstrate that the circuit behavior is strongly influenced by the power supply voltage. At $V_{cc} < 13V$, the behavior correspondents to a periodic oscillator. The interesting operation V_{cc} -region is between 13V and 18V.



Fig. 2. Signals O_1 and O_2 , for $V_{cc} = 13.5V$, with the values of the components shown in. Fig. 1



Fig. 3. Signals O_1 and O_2 , for $V_{cc} = 15V$, (left), and the corresponding phase plot (right)



Fig. 4. Two screenshots with the outputs O_1 and O_2 (left and middle) and phase plot (right), for $V_{cc} = 16V$



Fig. 5. Signals O_1 and O_2 (left) and phase plot (right), for $V_{cc} = 18.5V$

III. Conclusion

The circuit constitutes an easy to build example of chaotic system based on relaxation circuit and can be assembled in class by students in less than 10 minutes on a breadboard. The operation is stable because this circuit is less plagued, as many chaotic circuits are, by high sensitivity to the component tolerances. Thus, the circuit is a good choice for introducing chaotic circuits operation, chaotic behavior and methods of chaos characterization to students, even for vividly immersing K12 students into chaos and nonlinear dynamics concepts, through teaching by investigation, experimenting and active engagement. The circuit can be also used in a variety of applications, including audio noise effects.

Acknowledgment

This research is partly the result of the cooperation between "D. Ghitu" Institute of the Electronic Engineering and Nanotechnologies of the Academy of Sciences of Moldova, "Gheorghe Asachi" Technical University of Iasi, Romania, and the Institute of Computer Science of the Romanian Academy.

Authors' contributions

The circuit was proposed by the second author who also carried out the planning of the work and the method of analysis and implementation. The first author implemented and simulated the circuits under the supervision and with the support of the first author.

IV. References

1. Ricardo Aguilar-López, Rafael Martínez-Guerra, Chaos suppression via observer based active control scheme: Application to Duffing's oscillator. Chaos, Solitons & Fractals, Volume 32, Issue 5, June 2007, Pages 1887-1897.

2. B.B. Sharma, I.N. Kar, Stabilization and tracking controller for a class of nonlinear discrete-time systems Original Research Article Chaos, Solitons & Fractals, Vol. 44, Issue 10, October 2011, Pages 902-913.

3. Chun-Kai Cheng, Hang-Hong Kuo, Yi-You Hou, Chi-Chuan Hwang, Teh-Lu Liao, Robust chaos synchronization of noise-perturbed chaotic systems with multiple time-delaysOriginal Research Article Physica A: Statistical Mechanics and its Applications, Vol. 387, Issue 13, 15 May 2008, Pages 3093-3102.

4. <u>http://www.ti.com/lit/ds/symlink/lm555.pdf</u> accessed February 2012.

5. H.N. Teodorescu, A New Class of Chaotic Circuits based on Capacitive Feedback. Proc. ITEI, Kishnew, R. Moldova, May 20-22, 2010.

6. Teodorescu H.N., Cojocaru V., Complex Signal Generators based on Capacitors and on Piezoelectric Loads, Chaos Theory Modeling, Simulator and Application, World Scientific Publishing 2011 pp. 423-430.

7. H.N. Teodorescu, F. Iftene, Increasing the Operation Security of a Microsystem with a Chaotic Oscillator. ISIIE Conference Galati, Sept. 2010.

8. Y. Kobayashi, H. Nakano, T. Saito, A Simple Chaotic Circuit with Impulsive Switch Depending on Time and State. Nonlinear Dynamics (2006) 44: 73–79, Springer 2006.

9. K. Mitsubori and T. Saito, Dependent Switched Capacitor Chaos Generator and Its Synchronization. IEEE Trans. Circuits and Systems-L: Fundamental Theory and Applications, Vol. 44, No. 12, pp. 1122-1128, Dec. 1997.

10. A. Algaba, E. Freire, E. Gamero, A.J. Rodríguez-Luis, Analysis of Hopf and Takens– Bogdanov Bifurcations in a Modified van der Pol–Duffing Oscillator. Nonlinear Dynamics vol. 16, pp. 369–404, 1998. Kluwer Academic Publ.